

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Dominic Plunkett

SERIAL NUMBER:

10/699,606

FILED:

October 31, 2003

TITLE:

"Testing of Integrated Circuits Using Boundary Scan"

**GROUP ART UNIT:** 

2855

ATTORNEY DOCKET NO.:093053.00001

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

<u>CERTIFICATE OF MAILING</u>: I hereby certify that this document and all referenced attachments were placed in an envelope and deposited with the U.S. Postal Service with sufficient postage for First-Class Mail, addressed to the Commissioner for Patents at the address indicated on this page, on this the 21<sup>st</sup> day of April, 2004.

Valli E. Haas

## SUPPLEMENTAL PRELIMINARY AMENDMENT

Dear Sir or Madam:

Please amend this application as follows:

Amend the drawings by replacing the current sheet 2 (FIG 2) with the attached Replacement Sheet.